Remove R295
- 8.66K
Remove R537
- 0 V
Remove C292
and Short
Remove C493 and replace
with 3.01K resistor
DC4227 is good for 36V Rail-Rail

Rack Voltages are set 5V above and below input levels

Gain=1.5

Vou=Vn(1-R1/R0)

Gains are set to match each bias voltage need

DAC Output is set by VREFH & VREFL

Capacitor must be NP0/C0G or Tantalum

Vref BIAS CHANNELS 28-31
WRITE Memory Location 0x23C-23F
+5V

DPA4277 is good for 36V Rail-Rail
Rail Voltages are set 5V above
and below Input Levels

Vout=Vref1-Rf/Rb
Gain=1.5

-Vr1-0

-Gains are set
to match each
bias voltage need

DAC Output is set
by VREFh & VREFl0

Capacitor Must Be
NP0/C0G or Tantalum

Vr BIAS CHANNELS 24-27
WRITE Memory Location 0x238-23B
NOTE: For all ADCs (qty 12) on Channels 0-11, a 10 uF capacitor (ECJ-4YF1H106Z 1210 50V Ceramic or equivalent) is added across pins 36 and 37.

Capacitor used in production mods is: ECJ-2FBOJ106M 0805 6V Ceramic

See DocDb 4345 - ECO 1 to fix DNL on two LSBs.
Remove C314 and short

Remove C490 and replace with 3.01K

Gain as Needed

Vin = (+/-12.5) V/RT

ECO # 3 - connect pin 4 of U89 to +15V

ECO # 2 - DOCdb 4346 6.88K

ECO # 4
Remove R539
and replace
with 3.01K

Remove R555
and replace
with 3.01K

Remove C310
and short

Remove C312
and short

Remove C502
and replace
with 3.01K

Remove C503
and replace
with 3.01K

Gain as Needed
Vout = (+/-)2Vd1/Rf/Ri

Gain 10

Gain 25
TELEMETRY CHANNELS BIAS 40-47
READ Memory Location 0x46B-46F

loading=22.5k
per channel
TELEMETRY CHANNELS: BIAS 32-39
READ Memory Location 0x460-467

loading: 22.5K per channel