NOTES:
1. Before installing any components check all boards using an ohm meter to verify open circuit between all power and ground nodes. Check all triangles, (lobe/edges) for shorts between them.
2. All polarized caps are marked with a plus (+) sign on the positive node.
3. All diodes have a bar marking the cathode side.
4. All holes and lands of uninstalled components shall be kept free of solder.
5. Trim leads on bottom of board <= .079 inches tall.
6. Author dimensional locations apply to both the top and the bottom of the board.

8. Leads of PCI connectors J1, J2, J5, J6 are to be soldered.
9. Cut pin 3 of J1B for keying, only gold side.

ASSEMBLY
TOP