DESCRIPTION OF MODIFICATION:

1. Change: \( R_{137} = R_{138} = R_{125} = R_{122} = 221 \text{k}\Omega \) – Increased bandwidth by decreasing the input amplifier low pole frequency to reduce sample sag during the integration time.

2. Change: \( R_{246} = R_{221} = R_{199} = R_{175} = R_{250} = R_{225} = R_{203} = R_{180} = 3.32 \text{k}\Omega \) – Increased integrator time constant.

REASON FOR MODIFICATION:

Special Analog Front End for 0.9m Tek – 2k.

Optimize frequency response of the input filter and the time constant of the integrator of both boards.