NOTES:

1. This assembly is ROHS compliant.
2. Before installing any components check all boards using an ohm meter for shorts between all power and ground nodes. All power nodes can be found on J17.
3. All ground loops shall be spliced off board surface by 250–300 mils.
4. All traces of uninstalled components shall be kept free of solder.
5. Do not cover designated areas with overlay labels or markings. Use a blank area near board identification text for vendor label and marking placement.
6. If modifications are performed on this board, the testing process should be confined to the modified area. Do not subject the board to any off-spec testing processes.